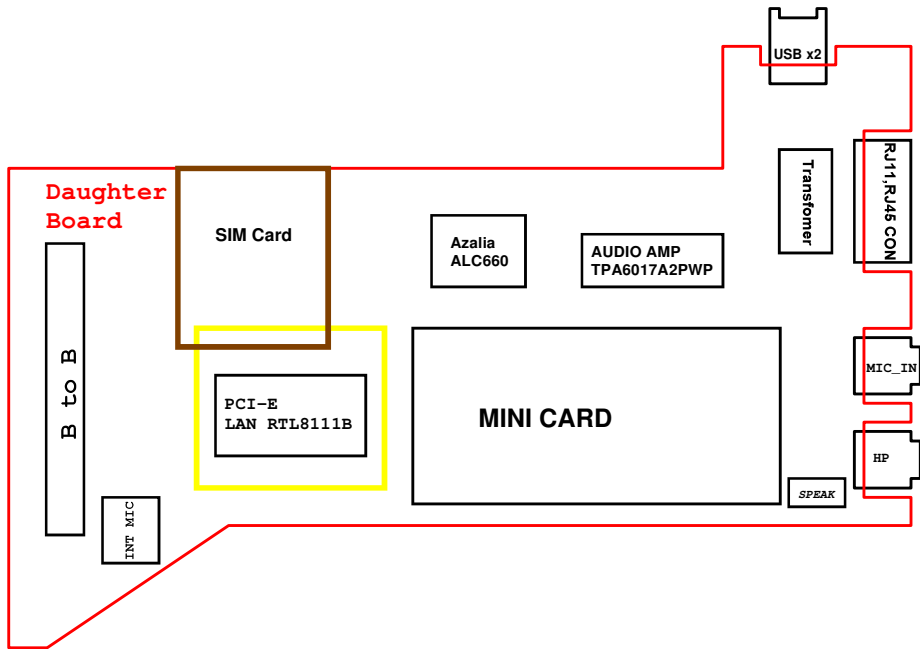


F9Dc I/O Board SCHEMATIC Rev 2.0

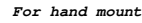
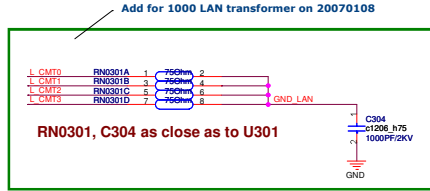
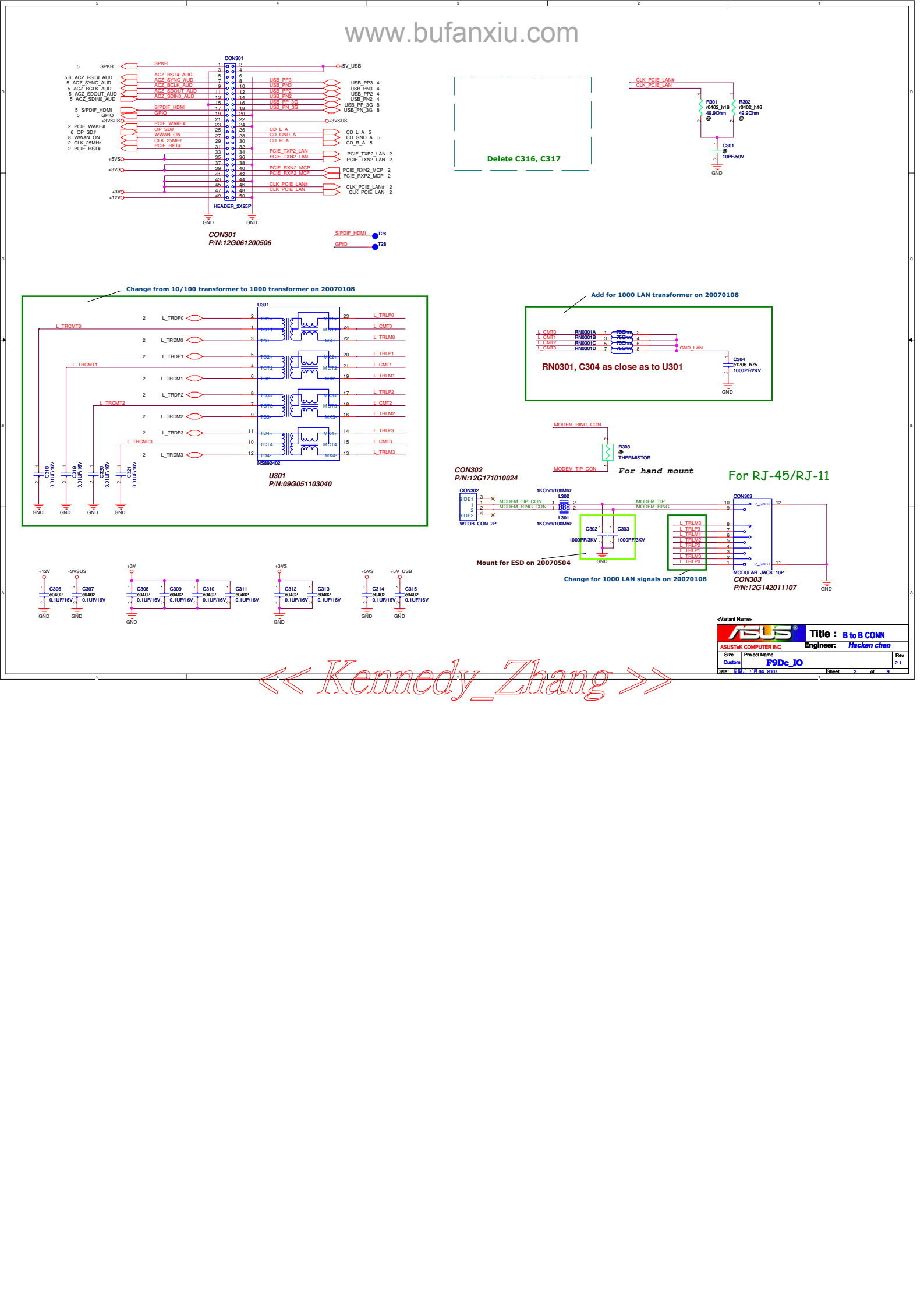
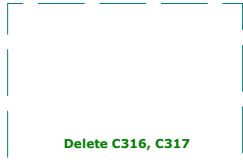
PAGE	Content
SYSTEM PAGE REF.	
1	INDEX
2	PCI-E-LAN_RTL8111B
3	B to B CONN(F)& TRANSFORMER
4	USB PORTS+MDC
5	AZALIA - ALC660-GR
6	AUDIO_AMPLIFIER
7	MICROPHONE
8	PCI-E--MINI CARD/SIM
9	History



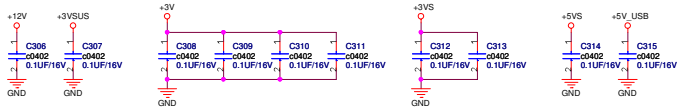
<< Kennedy_Zhang >>

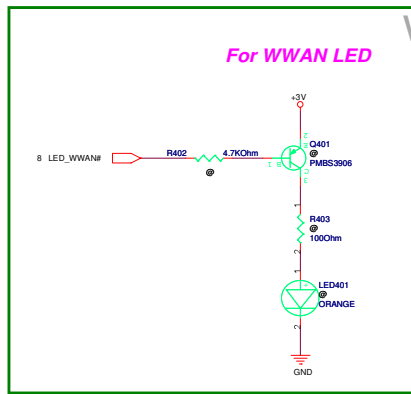
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		Title : INDEX	
ASUSTeK COMPUTER INC		Engineer: Hacken Chen	
Size	Project Name	Rev	
Custom	F9Dc_I0	2.1	
Date: 2007.04.04	Sheet 1	of 9	

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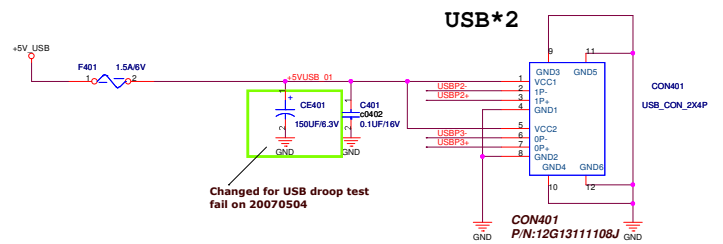
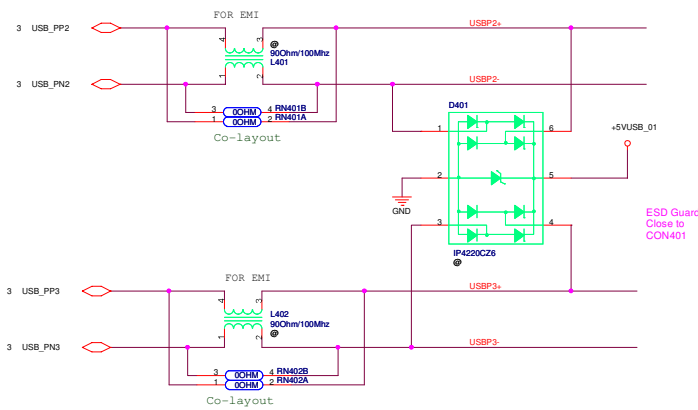


CON302
P/N:12G171010024





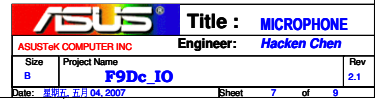
Only for test, please place as close as to the side of PCB



ASUS		Title : USB PORTS	
ASUSTek COMPUTER INC		Engineer: Hacken Chen	
Size	Project Name	Rev	
Custom	F9De_10	2.1	
Date: 2012.04.04	Sheet 4 of 9		

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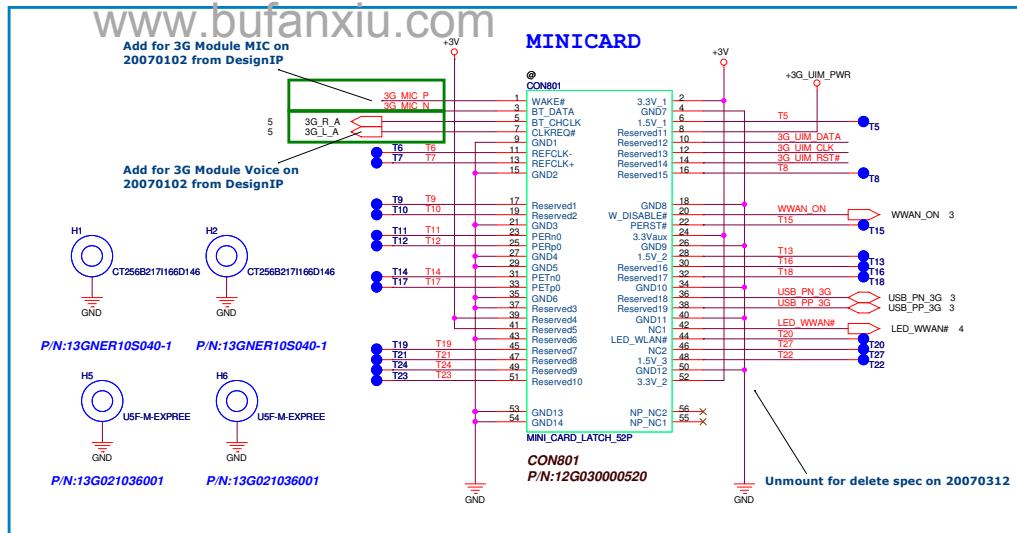
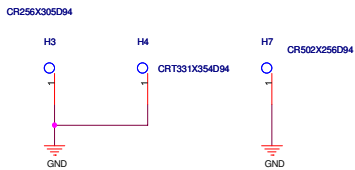


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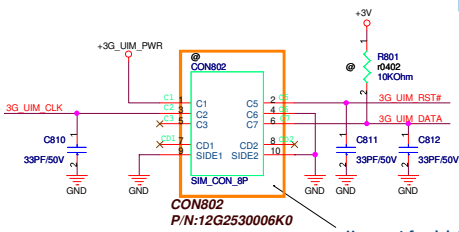
POWER CONSUMPTION:
+3VS: +3.003V~+3.597V
Max= 750 mA

+1.5VS: +1.425V~+1.575V
Max= 375 mA

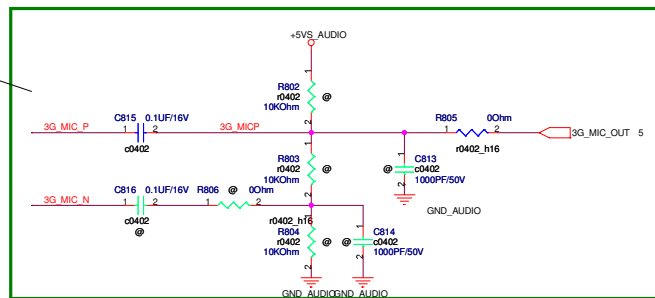
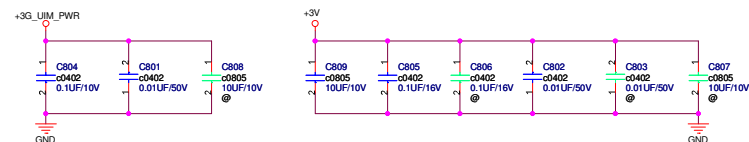
+3VAUX_GOLAN: +3.003V~+3.597V
Max= 250 mA



Add for 3G Module MIC on
20070102 from DesignIP



Unmount for delete spec on 20070312



<Variant Name>


		Title : PCI-E-MINI CARD/SIM	
ASUSTeK COMPUTER INC		Engineer: Hacken Chen	
Size B	Project Name F9Dc_IO		Rev 2.1
Date: 星期四 五月 04, 2007		Sheet 8 of 9	

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1. Rev1.00 on 20061031
2. Change CON603 from P/N: 12G140001089 to 12G14000108Y on 20061103
3. Change CON701 from P/N: 12G14030106E to 12G14030105N on 20061103
4. CON701 is changed from normal open to normal closed on 20061103
5. Add a LED circuit for WWAN with LED401, Q401, R402, R403 on 20061109
6. Change R301 ~ R304 from 10 ohm to 0 ohm on 20061110
7. Modify circuit for SPDIF and HDMI on 20061112
8. Add U503, U504, Q501, R513 for SPDIF and HDMI signal output on 20061113
9. Modify R214, R215 from 0402 to 0603 on 20061113
10. Modify and change CON301 pin define on 20061115
11. Add C519, C520, C521 for CD_R, CD_G, CD_L on 20061115
12. Add R514, R515, R516, R517, R518, R519 for CD_R, CD_G, CD_L on 20061115
13. Add C306 ~ C315 for PWR close to CON301 on 20061115
14. Add C205 and C207 for PCI-E LAN TX signals on 20061116
15. Add RXCT_R & TXCT_R for LAN Transformer to RJ45 om 20061117
16. Remove C218 ~ C222, C234 ~ C236, R511 on 20061120
17. Swap USB signals on 20061120
18. Modify R801 pull-high from +3G_UIM_PWR to +3V on 20061121
19. Change L701, L702 from 0603 to 0402 on 20061121
20. Change C509, C516, C706 from 0805 to 0603 on 20061121
21. Change R517, R518, R519 from GND_Audio to GND on 20061121
22. Change R514 ~ R519 from 0603 to 0402 on 20061120
23. Change C512 ~ C515, C519 ~ C521 from 0603 to 0402 on 20061120
24. Change R601 ~ R605, R626, R627, C605, C610 from 0603 to 0402 on 20061120
25. Change CON301 pin for +3V and +3VS on 20061124

- Rev1.1 on 20070108
- Rev Change List :
1. Add for 3G Module MIC on 20070102 from DesignIP
 2. Add for 3G Module Voice on 20070102 from DesignIP
 3. Add R520, R521, C524, C525, R802, R803, R804, R805, R806, C813, C814, C815, C816 for 3G Module MIC on 20070102
 4. Add R522, R523 for 3G Module Voice on 20070102
 5. Change CON801 Pin1, Pin3 net to 3G_MIC_P, 3G_MIC_N on 20070102
 6. Change CON801 Pin5, Pin7 net to 3G_R_A, 3G_L_A on 20070102
 7. Change LAN chip from L2 to RTL8111B on 20070108
 8. Change LAN Transformer from 10/100 to 1000 on 20070108
 9. Simplify circuit for MUTE_POP# on 20070109 and change Q604 to 2N7002
 10. Remove D604, D605 on 20070111
 11. Change AUDIO AMP from G1420 to TPA6017A2PWP on 20070115
 12. Add R525, R526 for ALC888S 2 port SPDIF function on 20070122
 13. Remove C316, C317 for moat plane on 20070126
 14. Change R514, R515, R516 from N/C to on and R520, R521, R522, R523 from on to N/C on 20070202


- Rev2.0 on 20070305
- Rev Change List :
1. Modify SPDIF Net error on 20070305
 2. Modify BOM of R618 from 100K to 2.2M for Audio POP Noise on 20070305
 3. Unmount CON801 and CON802 for 3G Module disable on 20070305
 4. Add C627 with 1000pF on net (LINE2_JD) for EMI Solution on 20070308
 5. Change C618 from 100pF to 1000pF on 20070309
 6. Change R610 ,R611 from 12K to 22K ohm for Speaker Power on 20070312
 7. Remove T25 on 20070312
 8. Add C526, C527 for ESD issue on 20070313
 9. Change CE401 from 100uF/6.3V to 150uF/6.3V on 20070314
 10. Remove R401 for USB on 20070315

<Variant Name>			
		Title :	History
ASUSTeK COMPUTER INC.		Engineer:	Hacken Chen
Size	Project Name		Rev
Custom	F9De IO		2.1
Date:	2007.04.04	Sheet	8 of 8

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Rev2.0 ECN List :
1. Mount R606 with 0 ohm for Audio EAPD function on 20070504
2. Change C618 and C627 fro ESD issue on 20070504
3. Mount C302 and C303 with 100PF/3KV for ESD on 20070504

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<Variant Name>		
		Title : History_ECN
ASUSTeK COMPUTER INC		Engineer: Hacken Chen
Size	Project Name	Rev
Custom	F9De_10	2.1
Date: 8/24/2007 10:28:10 AM		Sheet 10 of 10